

INPUT**Frequency**5 MHz, $\pm 5 \times 10^{-7}$ **Level**+7 dBm ± 5 dB into 50 Ohms**OUTPUT****Frequency**

5 MHz, dual output

Level+10 dBm ± 2 dB into 50 ohms**STABILITY****Output Phase Noise L(f)****Free-Running**

1 Hz	-115 dBc/Hz
10 Hz	-145 dBc/Hz
100 Hz	-165 dBc/Hz
1 kHz	-170 dBc/Hz
10 kHz	-172 dBc/Hz

Aging $\pm 2.5 \times 10^{-8}$ per year after 90 days operating, typical**Temperature Stability** $\pm 1 \times 10^{-8}$ free-running from 0 to +50°C, (Ref. +25°C)**Harmonics**

-30 dBc

Sub-Harmonics and Products

-50 dBc

Non-Harmonic Spurious

-70 dBc

Phase Lock Alarm

TTL

Locked: +3.5 VDC to +5.2 VDC (Hi)

Out-of-Lock: +0.8 VDC max (Lo)

Phase Lock Voltage Monitor

Voltage monitor pin supplied

MECHANICAL**Dimensions**

2.5 x 3.5 x 0.8"

ConnectorsSMA's and solder pins on side
Feed-thru terminals for lock alarm,
supply and phase lock voltage
monitor**Packaging**

Machined aluminum housing

Mounting

Shock mount patterns on sides

Thru holes, 4 places

Threaded inserts on base, 4 places

POWER REQUIREMENTS**Supply Voltage**+15 VDC $\pm 5\%$ **Warm-Up Power**10 Watts at start-up for 5 minutes
at +25°C**Total Power**

6 Watts at steady state +25°C

ADJUSTMENT**Loop BW**

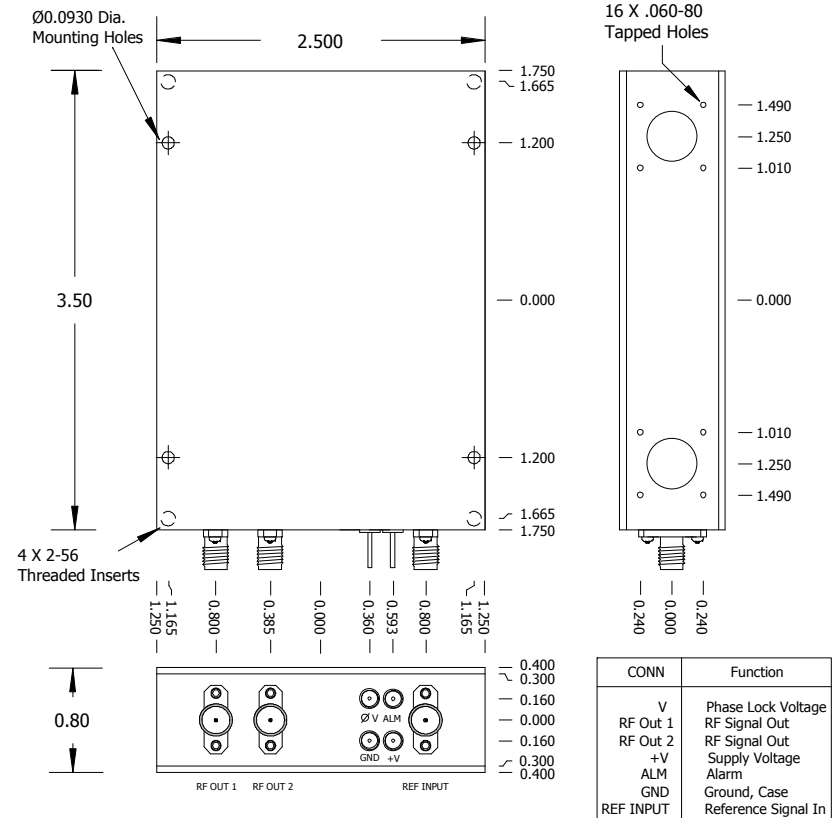
Target Bandwidth: < 1 Hz

Type 2 Loop,

< 5 minutes to $\pm 1 \times 10^{-9}$ of input**CRYSTAL****Type**

SC-cut

REV	DATE	REVISION RECORD	DWN	AUTH
-	12-06-02	Draft	Liz	LR
A	05-15-03	Updated Drawing	PAC	
B	11-11-04	Connectors, Drawing	SS	DC
C	01-10-05	Warm-Up and Total Power, Pin Dimensions	SS	LR

**Wenzel Associates, Inc.**

Austin, Texas

Title:

5 MHz-SC Dual Output Phase Lock Crystal Oscillator

P/N:

501-10226

Rev:

C

Date:

01-10-05

Drawn:

Ref:

Tolerances:
(except as noted)
Dimensions are in inches

0.XX Dec:

 ± 0.030 "

0.XXX Dec:

 ± 0.010 "

FSCM:

62821

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